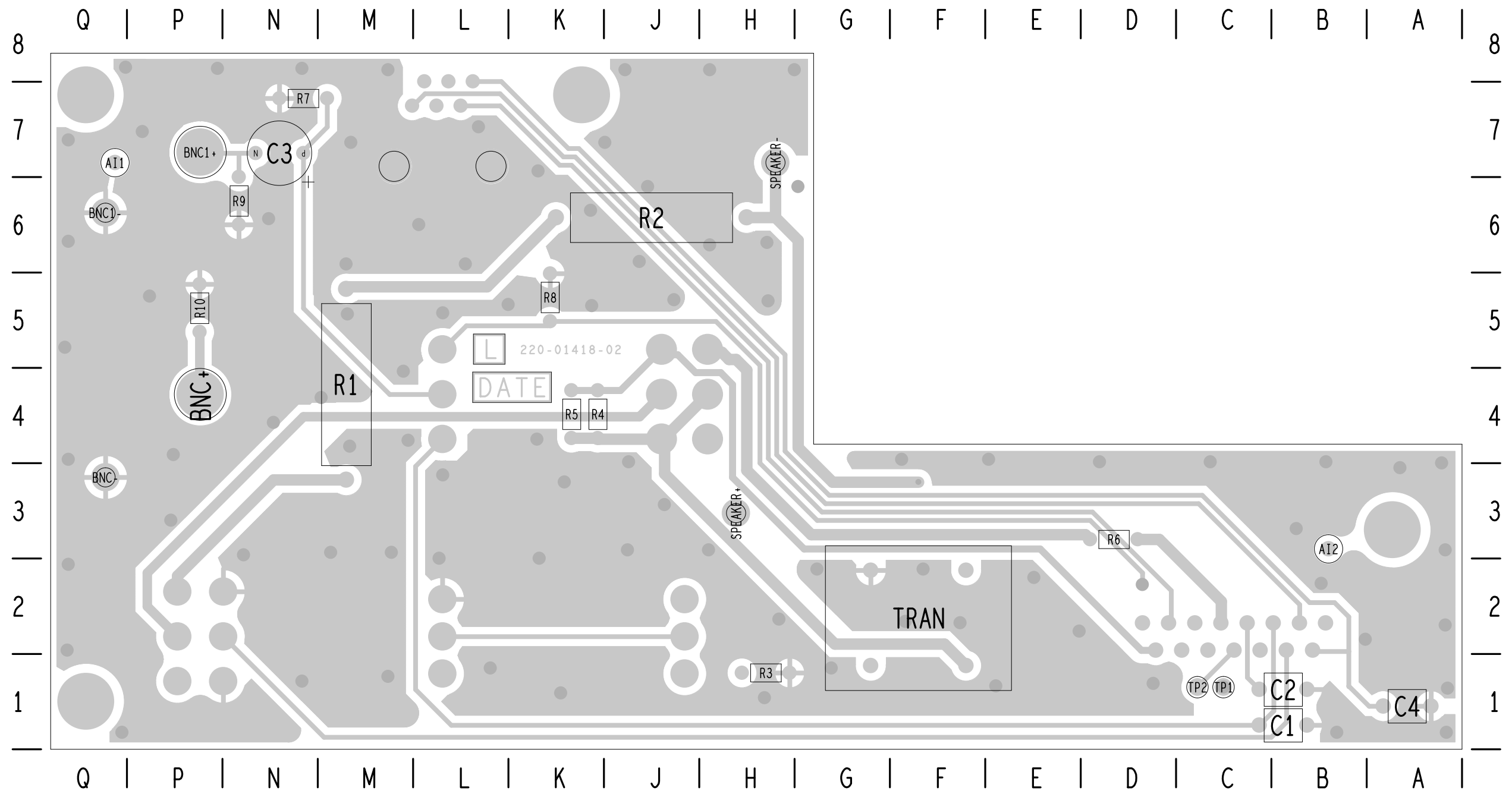

 <b>TAIT ELECTRONICS</b>	IPN:	AssyISS:	DATE:
	220-01418-02A	A	17Feb03
PHOENIX TM8000 Calibration PCB LAYOUT-PRIMARY-SIDE			

NOT TO SCALE



 <b>TAIT ELECTRONICS</b>	IPN:	AssyISS:	DATE:
	220-01418-02A	A	17Feb03
PHOENIX TM8000 Calibration PCB LAYOUT-SECONDARY-SIDE			

NOT TO SCALE